

Electronic Acknowledgement Receipt

EFS ID:	2107769
Application Number:	10618113
International Application Number:	
Confirmation Number:	8697
Title of Invention:	Wafer-level chip scale package and method for fabricating and using the same
First Named Inventor/Applicant Name:	Rajeev Joshi
Customer Number:	27966
Filer:	kenneth E. Horton/Hailey Walker
Filer Authorized By:	kenneth E. Horton
Attorney Docket Number:	11948.21
Receipt Date:	21-AUG-2007
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Information Disclosure Statement Letter	1194821.pdf	90824 c98469017532705769e02662a8f49177 a841ab5	no	2
Warnings:					
Information:					
2	Information Disclosure Statement (IDS) Filed	1194821IDS.pdf	798505 b2acac679e705d79b0e3d81aac12c4b7 1036133e	no	4
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8198 62f1f411-4e5b5e53-59a35a3c20431d1a c548b9cc	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			897527		
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